

Heterogeneous Integration Roadmap (HIR) Forum

at 2023 EPTC

December 8th, 2023

Agenda



1:20 pm - 1:25 pm	Welcome & Agenda Review	William (Bill) Chen and Ravi Mahajan
1:25 pm – 2:00 pm	Keynote Address	Choon Khoon Lim, Senior VP, ASM Pacific
2:00 pm – 2:10pm	HIR Briefing	William Chen
2:10 pm – 2:20 pm	2D-3D & interconnect	Ravi Mahajan
2:20 pm – 2:30 pm	Mobile	Benson Chan
2:30 pm – 2:40 pm	Modelling & Simulation	Chris Bailey
2:40 pm – 2:50 pm	Co-Design	Jose Schutt-Aine
2:50 pm – 3:00 pm	20 minutes Break	
3:20 pm – 3:30 pm	Supply Chain	Kitty Pearsall
3:30 pm – 3:40 pm	Integrated Power Electronics	Patrick McClusky
3:40 pm – 3:50 pm	SIP & Module	Erik Jung
3:50 pm - 4:00 pm	Test	George H to nominate
4:00 pm – 4:10 pm	HIR Workshop Wrap-Up	William Chen & Ravi Mahajan

Welcome to the Heterogeneous Integration Roadmap Forum at EPTC 2023



William Chen
ASE Group



Kitty Pearsall
Boss Precision



Ravi Mahajan
Intel



Chris Bailey
Arizona State Univ



Patrick McCluskey
Univ of Maryland



Erik Jung
Fraunhofer IZM



Benson Chan
Binghamton Univ



Jose Schutt-Aine
Univ of Illinois

